

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 05-266272

(43)Date of publication of application : 15.10.1993

---

(51)Int.Cl. G06K 19/07  
G06F 1/24

---

(21)Application number : 04-063777 (71)Applicant : FUJITSU LTD

(22)Date of filing : 19.03.1992 (72)Inventor : SATO TOSHIO

---

### (54) SYSTEM FOR RESETTING NONCONTACT IC CARD

#### (57)Abstract:

PURPOSE: To unnecessitate reset work after manufacturing and to stop runaway by setting the control part of an IC card at a reset state by a reset circuit and releasing the reset state by an operation signal from an MPU.

CONSTITUTION: The reset circuit 12 when receiving a start-up signal from an antenna 21 on a card processor 2 side is operated so as to be turned off for constant time. When a reset OFF state is set the operation of a control part program in the IC card is started and the operation signal is outputted at constant period as performing communication with the card processor 2 and the reset circuit 12 prevents the control part of the IC card 1 from being set in the reset state. When the communication with the card processor 2 side is completed and an operation is completed in a normal operation no operation signal is outputted and the reset circuit 12 sets the IC card in the reset state and the operation of the program can be started again by the start-up signal on the card processor 2 side. When a runaway state occurs no operation signal is outputted which stops the runaway.

---

### CLAIMS

---

#### [Claim(s)]

[Claim 1] In a resetting system of a noncontact IC card which has a reset circuit which operates with a seizing signal by an electric wave and light by the side of a card processor If a reset circuit is constituted so that a control section of an IC card may usually be made into a reset state a seizing signal from the card processor side cancels a reset state and an IC card is operated A resetting system of a noncontact

IC card preventing that output an actuating signal with a constant period in normal operation and a reset circuit makes a control section of said IC card a reset state as for a control section of said IC card.

[Claim 2] A resetting system of the non-contact IC card according to claim 1 wherein a period when said control section has prevented that a reset circuit makes this control section a reset state disregards a seizing signal from the card processor side.

[Claim 3] A resetting system of the noncontact IC card according to claim 1 or 2 canceling a reset state only when said seizing signal is more than predetermined pulse width.

---

## DETAILED DESCRIPTION

---

[Detailed Description of the Invention]

[0001]

[Industrial Application] This invention relates to the resetting system of the noncontact IC card which can be used for the passage check of a gate etc.

[0002]

[Description of the Prior Art] By creating a commuter pass by an IC card and turning an IC card to the ticket examining machine which distinguishes this like a commuter pass in the state where it separated several 10 cm as the automatic wicket method of a station A ticket examining machine is a commutation range or it judges whether the date etc. are effective and if effective a gate is opened research of the method of passing etc. is done and the announcement of these data is also made.

[0003] As a correspondence procedure of such a noncontact IC card and a card processor In the electromagnetic induction type which makes communication media the magnetic field generated by hundreds of kHz wave band radio the microwave method which makes several gigahertz microwave communication media and the near infrared ray there are an optical communication type etc. which carry out communication media and the communication range is performed within several meters from several 10 cm. The IC card and the main part had an antenna and have transmitted and received above-mentioned communication media from this.

[0004]

[Problem(s) to be Solved by the Invention] In the noncontact IC card using such communication media it was resetting by manual operation at the time of IC card new work. New work of a card is connecting a cell to the printed circuit board circuit of a card and changing into the state of powering on here and a power supply is not disconnected henceforth. In this case it is necessary to perform initial setting after powering on. It is this initial setting's being called reset and making a stopped clock and MPU into an operating state. In the unused state a noncontact IC card stops a clock and MPU and is attaining power-saving. That is in a noncontact IC card whenever

it is used with a power upreset is required. By reset a program performs initialization of a register and a work region and serves as command waiting from an antenna. After new work of a card in the state where reset is not carried out since it is in the state which is not understood where the program is running by this state it will not be in the state of accepting the signal from an antenna. It comes to run by resetting from the place where a program is normal.

[0005] At the time of new work the control section of an IC card will not be in a reset state but the conventional noncontact IC card resets one sheet at a time by manual operation. That is if reset always works and the control section operates once a reset circuit will commit the conventional view. It is because development will be easy and a program will also become easy if it does in this way.

[0006] That is when operation of an IC card's own program starts by an IC card's own judgment. That is since it can be made a reset state according to directions of a program a reset state is canceled by the seizing signal from an antenna and can perform normal operation (operation performed according to the command from an antenna) but. Since it is not a reset state immediately after manufacturing an IC card newly it cannot operate even if it receives the signal of reset state release from an antenna. For this reason at the time of new design and manufacture the IC card needed to be made into an one-sheet one-sheet reset state by manual operation. Since an IC card usually has much manufacture number of sheets this manual operation serves as serious work. Since the function made into a reset state by an IC card's own judgment is lost when the program of an IC card furthermore runs recklessly That is since a control section's own state cannot be made into a reset state at the time of the operation finish of an IC card control section a reckless run will not be able to be stopped but it will be in an unusable state.

[0007] Drawing 5 is a figure showing work of the control section of the IC card of the conventional reset circuit. After manufacture since the reset circuit serves as OFF (state which cannot reset an IC card control section) if this is set to ON by manual entry operation and it makes an IC card control section into a reset state and is made to start an IC card control section will start normal operation. Since an IC card control section operates a reset circuit and makes an IC card control section a reset state when suspending normal operation the seizing signal from the card processor side cancels the reset state of an IC card control section and the IC card control section can start normal operation henceforth. Since a reset circuit is operated and an IC card control section becomes impossible to a reset state a reckless run stops however stopping when it runs recklessly.

[0008] This invention was made in view of the above-mentioned problem and does unnecessary the reset work by the manual operation at the time of manufacture and an object of this invention is to provide the resetting system of the noncontact IC card which can also stop a reckless run.

[0009]

[Means for Solving the Problem]Drawing 1 is a principle figure of this invention. In a resetting system of a noncontact IC card which has a reset circuit which operates with a seizing signal by an electric wave and light by the side of a card processorIf a reset circuit is constituted so that a control section of an IC card may usually be made into a reset statea seizing signal from the card processor side cancels a reset state and an IC card is operatedA control section of said IC card prevents that output an actuating signal with a constant period in normal operationand a reset circuit makes a control section of said IC card a reset state.

[0010]A period when said control section has prevented that a reset circuit makes this control section a reset state disregards a seizing signal from the card processor side.

[0011]A reset state is canceled only when said seizing signal is more than predetermined pulse width.

[0012]

[Function]Manufacture of an IC card constitutes the reset circuit so that the control section of an IC card may always be made into a reset state. For this reasonit is not necessary to make one sheet at a time into a reset state. If a seizing signal is received from the antenna by the side of a card processora reset circuit will be committed so that it may be considered as the fixed time OFF (state which cannot reset the control section of an IC card). It has prevented that output an actuating signal to a reset circuit with a constant periodand a reset circuit makes the control section of an IC card a reset statethe control-section programing operation of an IC card being startedand communicating the card processor side by considering it as a reset OFF state. Since an actuating signal is no longer taken out after the communication by the side of a card processor is completed and ending by normal operationa reset circuit makes the control section of an IC card a reset stateand the programing operation of the control section of an IC card is again started with the seizing signal from the card processor side. When an IC card is in a runaway statesince an actuating signal is no longer taken out from the control section of an IC carda reset circuit makes the control section of an IC card a reset stateand since it is considered as a halt conditiona reckless run stops.

[0013]The control section of an IC card outputs an actuating signal with a constant periodand while having prevented that a reset circuit makes the control section of an IC card a reset statethe seizing signal from the card processor side is disregarded. if it does not do in this way -- under reset release (an IC card -- working) It is because it will be reset by the degree which is command reception and data communications become impossiblewhen the command from a card processor resembles the seizing signal in the data communications performed.

[0014]Since the card processor side and the IC card are communicating by the electric wave etc.they tend to mix disturbance. For this reasonthe following

[ predetermined pulse width ] consider it as noiseand are kept from canceling a reset

state by this noise as a seizing signal.

[0015]

[Example] Hereafter the example of this invention is described with reference to drawings. Drawing 2 is a block diagram showing the composition of this example. 1 is an IC card and 2 is a control device of the card processor which processes IC card 1. Although IC card 1 restores to the signal inputted from the antenna 20 of the control device 2, the antenna 10 which communicates and this antenna 10 and usually makes MPU16 the reset state with the modulation and demodulation circuit 11 which modulates the signal outputted from this antenna 10. The reset circuit 12 which operates so that MPU16 may not be made into a reset state when there are a seizing signal from the control device 2 and an actuating signal from MPU16 mentioned later. The serial/parallel-conversion machine 13 which changes the serial data to input into parallel data and changes the parallel data to output into serial data. It comprises ROM14 and RAM15 which store a program and data. MPU16 executes a program based on an input signal and the result is made to output from the antenna 10. The clock 17 which supplies timing in IC card 1 and the cell 18 which supplies electric power.

[0016] Drawing 3 is a circuit diagram of the reset circuit 12. Drawing 4 is a timing chart of the reset circuit 12. With reference to both figures, operation of the reset circuit 12 is explained below. The reset circuit 12 is controlled by the seizing signal which received the data from the antenna 20 and to which it restored and the actuating signal which MPU16 outputs with a working constant period.

[0017] When IC card 1 is in a reset state (state produced by the usual operation of the reset circuit 12), a break signal fulfills AND conditions from the modulation and demodulation circuit 11 and "\*\* working control" signal will be in a LOW state by the OR condition and NAND conditions for it to be more than the pulse width decided by  $T2=R2$  and C2. Since \*STBY serves as HIGH simultaneously, the oscillation operation of MPU16 is started. Reset is canceled after the time of  $T1=R1$  and C1 where this oscillation operation is stabilized and operation of a program is attained. If a 1-time "\*\* working control" signal serves as LOW in order to forbid reset by the signal from the antenna 20, this LOW signal is put into the break signal to input as AND conditions via the inverter 126, 127 and it is considered as "prohibition on \*\* reset."

[0018] If reset is canceled, MPU16 will output a trigger of operation to OR gate 124 with a constant period (less than T1) and will maintain "\*\* working control" signal at a LOW state. If MPU16 stops and \*\* operation trigger is no longer outputted,  $T4=R4$  and 4 hours after C\*\* working control signal will serve as HIGH and will be in a reset state. Thereby, if MPU16 stops, MPU16 will certainly be in a reset state. Since MPU16 will be in a reset state even if a program runs recklessly and MPU16 stops outputting \*\* operation trigger, it stops and a reckless run serves as seizing signal waiting from the card processor 2.

[0019] Next, work of each formation part of a circuit is explained. When the filter circuit which comprises R2 and C2 has the pulse width of the seizing signal from the antenna

20 smaller than the damping time constant of  $T_2=R_2$  and  $C_2$  it considers that it is a noise and removes. The circuit which comprises  $R_4$  and  $C_4$  decides on time for “\*\* working control” signal of drawing 4 to serve as HIGH with the damping time constant of  $T_4=R_4$  and  $C_4$ .  $T_4$  is decided by the control action of an IC card. If short it becomes a burden of a program and if too long the time to a reboot will become long.

[0020] The circuit which comprises  $R_1$ ,  $C_1$  and  $D$  (diode) delays the time of the reset release to MPU16 with the damping time constant of  $T_1=R_1$  and  $C_1$ . This time performs oscillation and stabilization of a clock. The diode  $D$  changes MPU16 promptly into the state of the waiting for starting and also when the following seizing signal comes sufficient clock oscillation time is taken.

[0021] The reset action by the manual operation at the time of guard manufacture not only becomes unnecessary but in the case of this device it becomes unnecessary [ the reset action by the manual operation at the time of a changing battery ]. When the ambient air temperature of an IC card falls cell voltage falls and the data of a memory may be destroyed. If temperature rises in this state and a program is operated it may run recklessly but a reset function can work and can make it stop also to this reckless run.

[0022]

[Effect of the Invention] In this invention the reset circuit is usually made to make the control section of an IC card a reset state and the seizing signal and the actuating signal from MPU canceled reset so that clearly from the above explanation. Therefore there is no necessity for the reset action by the hand control after manufacture and even if a reckless run occurs the control section of an IC card can be made into a reset state and can be stopped.

---

## DESCRIPTION OF DRAWINGS

---

[Brief Description of the Drawings]

[Drawing 1] It is a principle figure of this invention.

[Drawing 2] It is a figure showing the composition of the example of this invention.

[Drawing 3] It is a reset circuit diagram.

[Drawing 4] It is a timing chart of this example.

[Drawing 5] It is a figure showing operation of a conventional example.

[Description of Notations]

1 IC card

2 Card processor

10 and 20 Antenna

11 Modulation and demodulation circuit

12 Reset circuit

13 Serial/parallel-conversion machine

14 ROM

15 RAM

16 MPU

17 Clock

18 Cell

---